

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली  
हौज खास, नई दिल्ली -110016  
(औद्योगिक अनुसंधान एवं विकास इकाई)  
INDIAN INSTITUTE OF TECHNOLOGY DELHI  
Hauz Khas, New Delhi-110016  
(Industrial Research & Development Unit)

No. IITD/IRD/RP04506G/

315602

Dated:05/09/2024

**Advertisement No.: IITD/IRD/210/2024**

Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा.

Title of the Project	Development of large area 152.4 mm x 152.4 mm ~23% efficient silicon heterojunction solar cells, and ~25% efficient tandem cells of 50 mm x 50 mm area (Project lead by IIT Delhi and joined with IIT Bhilai). (RP04506G)	
Funding Agency	Department of Science & Technology	
Name of the Project Investigator	Prof. Vamsi Krishna Komarala, Department of Energy Science & Engineering [email ID: spsinghin@gmail.com]	
Deptt./ Centre	Department of Energy Science & Engineering	
Duration of the Project	Upto:31/03/2026	
Post (s)	Consolidated fellowship / Pay-slab	Qualifications
Project Scientist (02)	Rs.47,790-49,220-50,700-52,220-53,790-55,400-57,060-58770-60530-62,350 /- p.m.plus HRA @27%	Ph.D. in Silicon-based solar cell fabrication with characterization and related TCAD simulations for device performance enhancement. or M. Tech. in Energy Engineering with 5 years of research experience in silicon surface preparation and solar cell fabrication/characterization. Knowledge of plasma-enhanced chemical vapour deposition and sputtering systems for thin amorphous silicon layers growth and transparent conducting oxide layers growth (preferably) and characterization. Silicon solar cell/device analysis by using various optical and electrical characterization techniques to enhance the device's performance. The candidate is able to characterize the amorphous silicon thin films using different characterization techniques like Fourier Transform Infrared Spectroscopy and Ellipsometre, Minority carrier lifetime measurements, and analysis. Device interface analysis with electrical characterisation techniques is also desirable. TCAD simulations of complete silicon solar cell device structure with various configurations for electron- and hole-selective layer. In the simulations, one is able to consider good optical models for light path length enhancements and recombination models followed by the transport of charge carriers at the various interfaces of a device. Able to integrate multiple thin layers with suitable optical and electronic properties for improving the device's performance.
Project Assistant (Tech.) (01)	Rs.29,200-31,200-33,200-35,800-38,400-41,000/-p.m.(consolidated)	Diploma with 5 years of experience/B. Tech. degree with 2 year experience Clean room facility of Class 1000 maintenance including chiller water, nitrogen generator, UPS backup, and any electrical services for operating the thin film deposition systems for the solar cell fabrication without any interruption.
Project Assistant (Admin.) (01)	Rs.29,200-31,200-33,200-35,800-38,400-41,000/-p.m.(consolidated)	B.A./B.Com with 2 years of experience in administrative-related activities.

		Project-related purchases, bills preparation for payment, report preparation, supply order preparation, and any other administrative activities to support the project. Good experience in Word document preparation and Excel software utilization.
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The candidates who are interested to apply for the above post should download **Form No. IRD/REC-4** from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. **by e-mail with advertisement No. on the subject line to Prof.Vamsi Krishna Komarala at email id: spsinghin@gmail.com**

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) alongwith the online interview details. Only short-listed candidates will be informed for online interview. In case any clarification is required on eligibility regarding the above post, the candidate may contact **Prof. Vamsi Krishna Komarala at email id: spsinghin@gmail.com**

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है. एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। **The last date for submitting the completed applications by email is 20/09/2024 by 5.00 p.m.**

सहायक कुलसचिव, आईआरडी

#### वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof.Vamsi Krishna Komarala, PI, Deptt. of Energy Science and Engineering
- Copy to Chairperson, DRC/CRC

*Handwritten signature and date: 20/09/2024*